



ISO 9001 CERTIFIED

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## OPERATION MANUAL

**ES-Key**  
**12 PDM module (4 selectable polarity outputs)**  
**with 4 Inputs (selectable polarity) and**  
**4 MFI Inputs**

P/N 610-00035





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REV	1.20
BY	GMC

PRODUCT GROUP	ES-Key	P/N	FSG-MNL-00129
PRODUCT	12 PDM module with MFI Inputs		

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## 1. Revision Log

Rev	Date	Approved	Changes
1.00	4-02-2015	GMC	Initial requirements
1.10	11-9-2015	GMC	Updated CAN message ID's
1.20	2-27-2017	GMC	Corrected switch settings for inputs 0-3 and Outputs 8-9



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## 2. Module Overview

### 2.1. Scope

The Power Distribution Module (PDM) is an ES-Key™ node designed to allow a designer the ability to utilize the product within the ES-Key™ electrical system network. The module responds to commands to activate its physical outputs and reports the state of its inputs back to the network. The module has 12 outputs of which 4 of the outputs can be polarity selectable or configured as polarity selectable inputs. The module also has 4 MFI (Multi Function Inputs) inputs that can be configured to be used as a digital, analog, or frequency inputs.

### 2.2. Part numbers

12 PDM With Inputs      Hale – p/n      610-00035

### 2.3. Default Configuration Setup

The default module configuration when the part is delivered will be set for 12 positive outputs set to a device ID of 1 see Table 1 and Table 2.

SW2			SW3			SW4		
Function	Mode	Position	Function	Mode	Position	Function	Mode	Position
IN 0    OUT 8	OUTPUT	UP	POL IN0	NEG	UP	DEVICE	TYPE 1	DOWN
		UP	POL IN1	NEG	UP	AUX	N/A	UP
IN 1    OUT 9	OUTPUT	UP	POL IN2	NEG	UP	AUX	N/A	UP
		UP	POL IN3	NEG	UP	AUX	N/A	UP
IN 2    OUT 10	OUTPUT	UP	POL IN4	NEG	UP	AUX	N/A	UP
		UP	POL IN5	NEG	UP	AUX	N/A	UP
IN 3    OUT 11	OUTPUT	UP	POL IN6	NEG	UP	AUX	N/A	UP
		UP	POL IN7	NEG	UP	AUX	N/A	UP

Table 1. Default Dip Switch Settings.

(Note: SW2 dip switches must be selected in pairs).

(Note: The shunt jumpers must be selected in pairs).

11		10		9		8	
H9	H2	H8	H3	H7	H4	H6	H5
Pos 1	Pos 1	Pos 1	Pos 1	Pos 1	Pos 1	Pos 1	Pos 1
POSITIVE		POSITIVE		POSITIVE		POSITIVE	

Table 2. Default Shunt Settings

(Note: Selection switches are only read on power up).

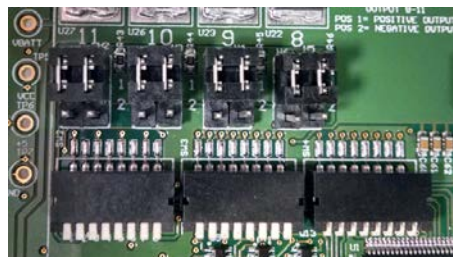


Figure 1. Dip Switch and polarity shunts



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### 3. Module Operation

#### 3.1. Input / Output Function

The module shares input 0-3 with outputs 8-11 using the same pins on the black 12 pin connector. Section 3.1.1 details how to configure the pin for the desired function.

Connector Position	Function	
PIN 1	Output 8	Input 0
PIN 2	Output 9	Input 1
PIN 3	Output 10	Input 2
PIN 4	Output 11	Input 3

Table 3.

#### 3.1.1. Input / Output Function Selection

Configure SW2 on the module to select the desired function for pins 1-4 on the black connector.

**Note: That SW2 must be configured in pairs and the switch number pair numbers change from software Version 1.7 to Version 1.8**

Pin Number	Switch Number Pairs		Switch Position	Function
	SW ver 1.7 And below	SW ver 1.8 and above		
1	Pos 1 and 8	Pos 8 and 7	UP	OUTPUT 8
			DOWN	INPUT 0
2	Pos 3 and 6	Pos 6 and 5	UP	OUTPUT 9
			DOWN	INPUT 1
3	Pos 5 and 4	Pos 4 and 3	UP	OUTPUT 10
			DOWN	INPUT 2
4	Pos 7 and 2	Pos 2 and 1	UP	OUTPUT 11
			DOWN	INPUT 3

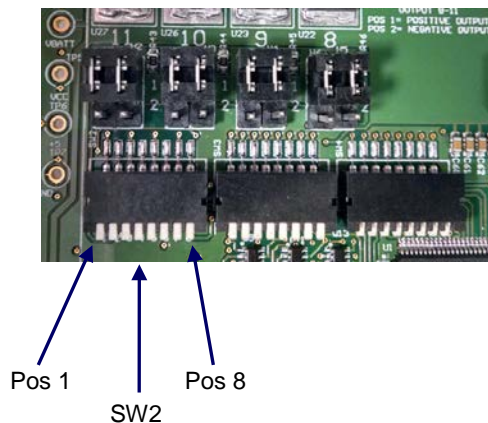


Figure 2 Dip Switch selection switches.

Table 4. I/O Function Settings

(Note: Selection switches are only read at power up).

### 3.2. Selectable polarity inputs

The module has digital inputs that can be configured for either positive or ground input (see section 3.1.1). An input is flagged as ACTIVE in the ES-Key database when the voltage level of the input is within the required range (refer to the table below). (refer to the table below).

Input Polarity	Input requirement
Positive	Input is flagged as ACTIVE when its voltage is greater than 60% of supply power.
Ground	Input is flagged as ACTIVE when its voltage is less than 40% of supply power.

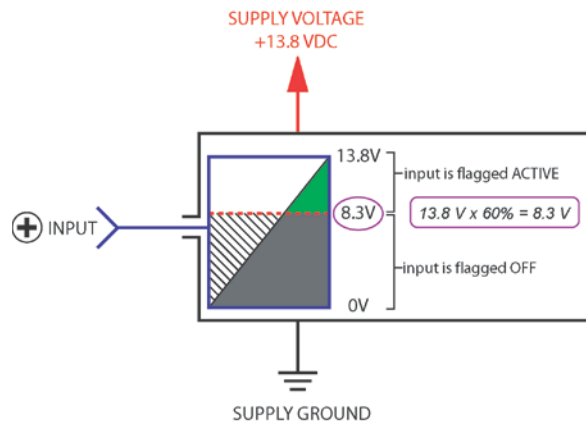


Figure 3. *Positive input example.*

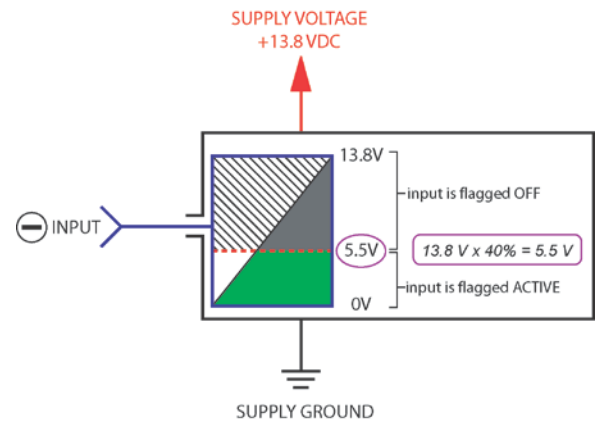


Figure 4. *Ground input example.*

The examples above illustrate the voltage range required for an input to be flagged as ACTIVE to the ES-Key database. The voltage range is based on the polarity of the input (positive or ground) and the voltage level of the supply voltage. In figure 1 the input is a positive polarity type, the supply voltage is 13.8 VDC, and the valid voltage range for the input is 8.3 VDC and greater (less than 8.3 VDC flags the input as OFF).



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### 3.2.1. Input Polarity Selection

The polarity of each input is selected by setting the polarity dip switch SW3 (located inside of the case) to the desired positions. **Error! Reference source not found.** shows the settings to select the input configurations of inputs 0 - 7. Note: Inputs 4-7 only work if configured as digital inputs (see section 3.3).

INPUT NUMBER	Switch Number	Switch Position	Input Polarity
0	1	UP	NEGATIVE
		DOWN	POSITIVE
1	2	UP	NEGATIVE
		DOWN	POSITIVE
2	3	UP	NEGATIVE
		DOWN	POSITIVE
3	4	UP	NEGATIVE
		DOWN	POSITIVE
4	5	UP	NEGATIVE
		DOWN	POSITIVE
5	6	UP	NEGATIVE
		DOWN	POSITIVE
6	7	UP	NEGATIVE
		DOWN	POSITIVE
7	8	UP	NEGATIVE
		DOWN	POSITIVE

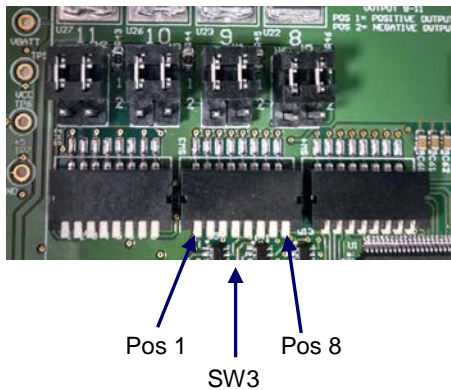


Figure 5 Dip Switch selection switches.

Table 5. Input polarity Dip Switch Settings

(Note: Selection switches are only read at power up).



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### 3.3. MFI inputs

Inputs 4-7 are MFI inputs. This gives the input the ability to be software controlled from a CAN message (see section 7.5 for details). Depending on how the input is configured will determine its operation. If the input is configured as a digital input it will report to the ES-Key™ electrical system network . if the input is configured any other way the value read on the input is transmitted out a CAN message see (section 6.5 for details). The MFI inputs can be configured for the following functionality.

- Digital Input configured for either positive or ground input (see section 3.1.1).
- Analog input with a range of 0 - 5 volts.
- Analog input with a range of 0 - 30 volts.
- Analog input with a range of 4 - 20 mA.
- Analog input as a Thermistor input.
- Frequency input



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### 3.4. Solid State Outputs

Each output of the PDM utilizes solid state, fully protected high-side drivers that feature overload protection, current limitation, open load detection and transient protection. These output drivers replace the requirement of a relay and circuit breaker. The module also has 4 low-side drivers that feature overload protection and transient protection (see section 3.3.3 for polarity selection).

*High Current Rating.* Each of the high current outputs is capable of supplying 6 Amps continuously on all outputs at 85° Celsius.

*Low Current Rating.* Each of the low current outputs is capable of supplying .25 Amps continuously on all outputs at 85° Celsius.

*Circuit Protection/Breaker.* If output current exceeds 6.5 Amps nominal the output will automatically turn off. The module will attempt to connect the output to the load two more times at 5 second intervals. If the output is still overloaded, then it will remain off.

The "circuit breaker" feature can be reset (or reinitialized) by de-activating the output through the ES-Key™ network - in the distributed network, any number of switches may be configured to deactivate the particular output. When the output is turned back on, the over current tests will be initiated.

When an output switch is in an over current situation, a fault is logged to the USM and data logger. The system fault light will be activated while any over current situation exists.

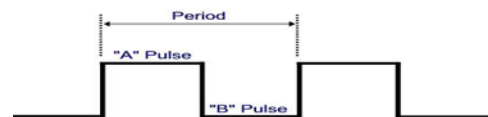
*Circuit Activation Detection/Diagnostics.* When an output driver is intended to be ON, and a load is not detected at the output, the system will generate a network tag to indicate open circuit ("no-load") for the specific output. The tag can be used by the ES-Key™ network for diagnostics or indication. The minimum load current to activate this tag is 4 amps nominal. During an over current shutdown condition, this tag will be active for the particular output.

Likewise, a separate tag is generated to indicate that a load is operating within the proper limits (see section 3.5).

#### 3.4.1. Flash outputs

The PDM outputs 0-7 have the capability to flash at two flash periods: 150 Hz and 75 Hz. Output memory spaces 12 - 19 control the output flash feature and output memory space 20 controls the flash period (see section 3.5). Activate an outputs flash output (output memory space 12-19) to begin flashing the output. The physical output (output memory space 0-7) should be OFF.

Outputs 0 - 3 flash on the "A" pulse, and outputs 4 - 7 flash on the "B" pulse. The period length is determined by the flash rate. This logic makes implementing alternating flashers quite simple.





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When the physical output is ON and its flash output is ON the flash occurs on the opposite pulse. For example, output 1 normally flashes on the “A” pulse, but when its flash output (output memory space 13) and its physical output (output memory space 1) are activated together the output flashes on the “B” pulse.

Output memory space 0	Output memory space 12	Result
OFF	OFF	Physical output 0 (pin 1) is OFF
ON	OFF	Physical output 0 (pin 1) is ON
OFF	ON	Physical output 0 (pin 1) is flashing on the “A” pulse
ON	ON	Physical output 0 (pin 1) is flashing on the “B” pulse

Output memory space 4	Output memory space 16	Result
OFF	OFF	Physical output 4 (pin 5) is OFF
ON	OFF	Physical output 4 (pin 5) is ON
OFF	ON	Physical output 4 (pin 5) is flashing on the “B” pulse
ON	ON	Physical output 4 (pin 5) is flashing on the “A” pulse

**3.4.2. Pulse Width Modulate (PWM) outputs**

Outputs 0-7 can be controlled ON at reduced power by activating its PWM output (see section 3.5).

Output memory space 0	Output memory space 24	Result
OFF	OFF	Physical output 0 (pin 1) is OFF
ON	OFF	Physical output 0 (pin 1) is ON
OFF	ON	Physical output 0 (pin 1) is ON at 60% PWM
ON	ON	Physical output 0 (pin 1) is ON (no PWM)

For example, as shown in the table above, if the output and PWM are activated the load for a physical output will be ON. To set the physical output to PWM (reduced power) mode it is necessary only to shed the primary output address for the desired output. The duty cycle for outputs 0-7 can be adjusted from a CAN message from 0 to 100 while the output is turned on (see section 7.4).



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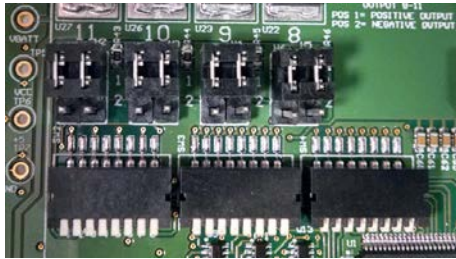
**3.4.3. Output polarity selection**

Output 0-3 are always positive the polarity of output 4-7 is selected by setting the output polarity shunts (located inside of the case) to the desired positions. Table 4 and 5 shows the settings to select the output configurations of outputs 4 - 7.

Output 11		Output 10		Output 9		Output 8	
Shunt H9-H2		Shunt H8-H3		Shunt H7-H4		Shunt H6-H5	
POSITIVE	POS 1	POSITIVE	POS 1	POSITIVE	POS 1	POSITIVE	POS 1
NEGATIVE	POS 2	NEGATIVE	POS 2	NEGATIVE	POS 2	NEGATIVE	POS 2

**Table 6. Output Shunt Settings.**

(Note: Selection switches are only read at power up).



**Figure 6. Output polarity selection Shunts.**



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**3.5. Module type and address**

The PDM is recognized by the ES-Key Professional software as a PDM module (device type 1), or as a *switch input/output* module (device type 4) depending on the position of the selector switch used for device type identification (see section 3.4.1).

**3.5.1. Device type selection**

The device type is selected by setting the selection switch (located inside of the case) to the desired position. The switch is labeled DEV ID and is directly related to the device type. When the switch is down it is a device type 1. When the switch is up it is a device type 4.

The address for both versions is selected by rotating the address switch to the desired value (0-15). Use an address selection tool (or a #1 Philips screwdriver) to set the position of the switch to the desired address.

(Note: Selection switches are only read at power up).



Figure 7. Selection switches.



Figure 8. Address switch



Figure 9 Address selection tool.



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### 3.6. Input/output memory space

The PDM uses standard ES-Key defined input and output memory space. The polarity selectable inputs are mapped into the input space, and the outputs are mapped into the output space.

INPUT MEMORY SPACE	
INPUT	DESCRIPTION
0	Physical input 0
1	Physical input 1
2	Physical input 2
3	Physical input 3
4	Physical input 4 (MFI Digital Mode only)
5	Physical input 5 (MFI Digital Mode only)
6	Physical input 6 (MFI Digital Mode only)
7	Physical input 7 (MFI Digital Mode only)
8	Output 0 active tag
9	Output 1 active tag
10	Output 2 active tag
11	Output 3 active tag
12	Output 4 active tag
13	Output 5 active tag
14	Output 6 active tag
15	Output 7 active tag
16	Output 8 active tag
17	Output 9 active tag
18	Output 10 active tag
19	Output 11 active tag
20	Output 0 circuit open tag
21	Output 1 circuit open tag
22	Output 2 circuit open tag
23	Output 3 circuit open tag
24	Output 4 circuit open tag
25	Output 5 circuit open tag
26	Output 6 circuit open tag
27	Output 7 circuit open tag
28	Output 8 circuit open tag
29	Output 9 circuit open tag
30	Output 10 circuit open tag
31	Output 11 circuit open tag

OUTPUT MEMORY SPACE	
OUTPUT	LOCATION
0	Physical output 0
1	Physical output 1
2	Physical output 2
3	Physical output 3
4	Physical output 4
5	Physical output 5
6	Physical output 6
7	Physical output 7
8	Physical output 8
9	Physical output 9
10	Physical output 10
11	Physical output 11
12	Flash output 0
13	Flash output 1
14	Flash output 2
15	Flash output 3
16	Flash output 4
17	Flash output 5
18	Flash output 6
19	Flash output 7
20	Flash period (ON = 150Hz, OFF = 75Hz)
21	<i>reserved</i>
22	<i>reserved</i>
23	<i>reserved</i>
24	PWM output 0
25	PWM output 1
26	PWM output 2
27	PWM output 3
28	PWM output 4
29	PWM output 5
30	PWM output 6
31	PWM output 7



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## 4. Connector Description

The module has two connectors and one power input stud. The following definitions apply:

### 4.1. Connector Pin Out

Mating connector: Deutsch DTM06-12SA (GRAY)		
Mating sockets: 0462-201-20141		
Wedge lock: WM12S Recommended wire gage: 18-24 AWG		
PIN	CIRCUIT	DESCRIPTION
1	Output 0	Digital output (positive polarity, 7.5A)
2	CAN High	ES-Key CAN, SAE J1939 Proprietary, 250 kbits/S
3	CAN Shield	ES-Key CAN, SAE J1939 Proprietary, 250 kbits/S
4	Output 2	Digital output (positive polarity, 7.5A)
5	Output 4	Digital output (positive polarity, 7.5A)
6	Output 6	Digital output (positive polarity, 7.5A)
7	Output 7	Digital output (positive polarity, 7.5A)
8	Output 5	Digital output (positive polarity, 7.5A)
9	Output 3	Digital output (positive polarity, 7.5A)
10	Output 1	Digital output (positive polarity, 7.5A)
11	CAN Low	ES-Key CAN, SAE J1939 Proprietary, 250 kbits/S
12	Supply -	Module supply (vehicle ground)

Mating connector: Deutsch DTM06-12SB (BLACK)		
Mating sockets: 0462-201-20141		
Wedge lock: WM12S Recommended wire gage: 18-24 AWG		
PIN	CIRCUIT	DESCRIPTION
1	Output 8 / Input 0	Digital output (positive polarity, 7.5A)
		Digital output (negative polarity, .25A)
		Digital Input (positive polarity)
		Digital Input (negative polarity)
2	Output 9 / Input 1	Digital output (positive polarity, 7.5A)
		Digital output (negative polarity, .25A)
		Digital Input (positive polarity)
		Digital Input (negative polarity)
3	Output 10 / Input 2	Digital output (positive polarity, 7.5A)
		Digital output (negative polarity, .25A)
		Digital Input (positive polarity)
		Digital Input (negative polarity)
4	Output 11 / Input 3	Digital output (positive polarity, 7.5A)
		Digital output (negative polarity, .25A)
		Digital Input (positive polarity)
		Digital Input (negative polarity)
5	MFI Input 4	Multi Function Input
6	MFI Input 5	Multi Function Input
7	MFI Input 6	Multi Function Input
8	MFI Input 7	Multi Function Input
9	+9 Volt Reference	Sensor Reference .25A
10	Ground Reference	Sensor Reference
11	+5 Volt Reference	Sensor Reference .25A
12	Ground Reference	Sensor Reference



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Mating terminal: #10 ring terminal  
 Recommended wire gage: 8 AWG (for maximum load on the 8 outputs)

PIN	CIRCUIT	DESCRIPTION
STUD	Supply +	Module supply (+9VDC...+32VDC)



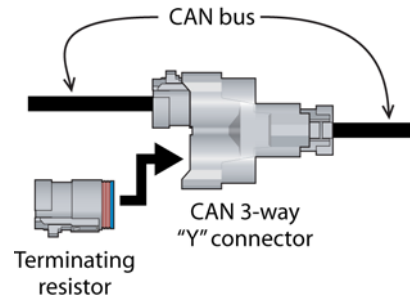
Figure 10. Connector identification.

**4.1.1. Terminating resistor requirement (CAN communication)**

Two terminating resistors (120 Ohm) are required on the CAN bus for proper operation (one at each end of the CAN bus). Only two terminating resistors are allowed on a CAN bus.


Terminating resistor p/n DT06-3S-P006

CAN 3-way "Y" connector p/n DT04-3P-P007



**4.2. System compatibility**

The PDM Module is compatible with other Class 1 CAN devices.

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<b>PRODUCT</b> 12 PDM module with MFI Inputs			<b>BY</b> GMC	

## 5. Mounting

### 5.1. Mounting dimensions

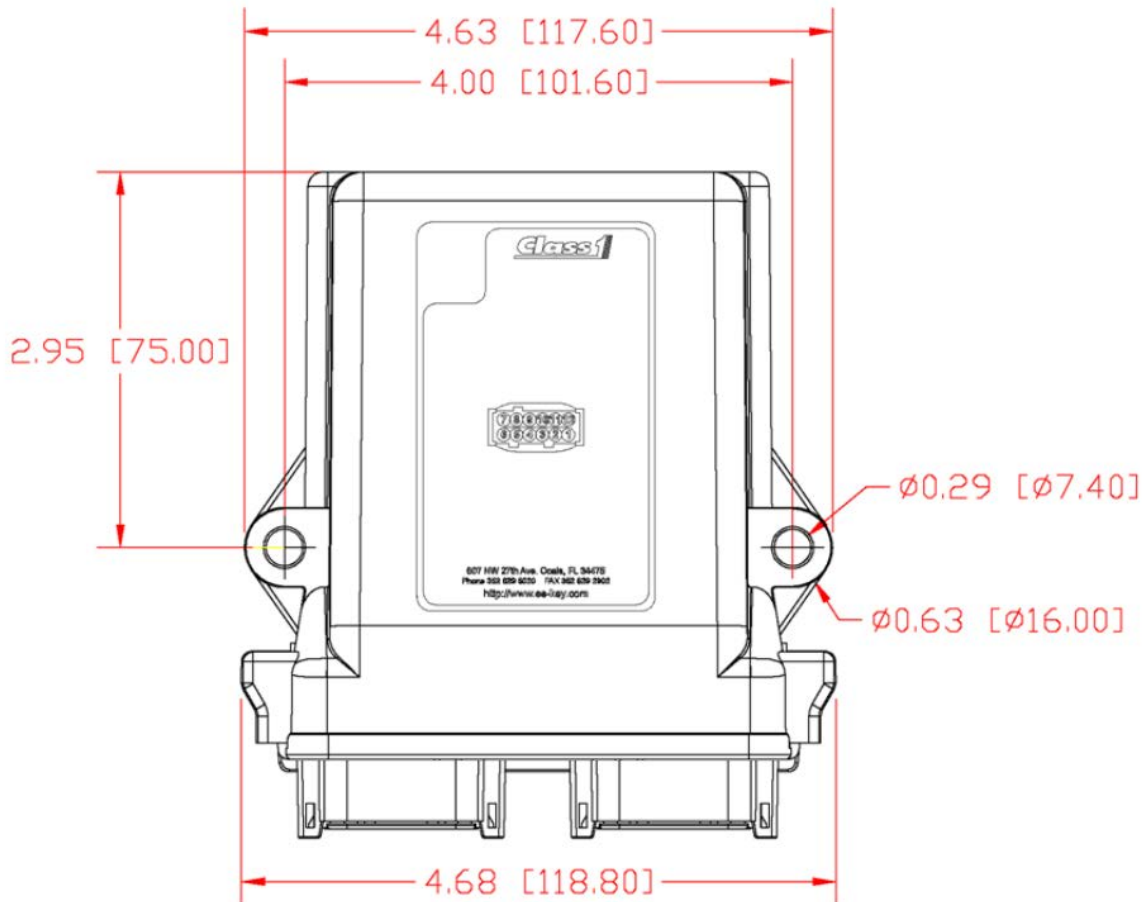


Figure 11. Mounting dimensions – inches [millimeters].

### 5.2. Mounting notes

When mounting the module vertically, make certain the connector is pointed down so as to eliminate the possibility of standing water in the connector.

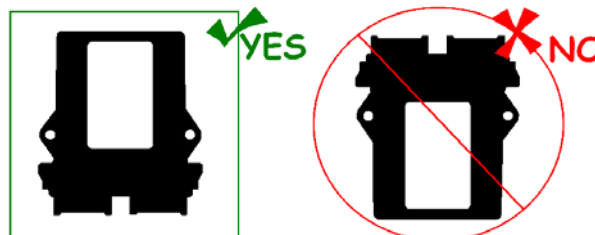


Figure 12. Vertical mounting requirement.





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## 6. Device Network TX CAN messages

The ES-Key device ID for the module is 1X<sub>h</sub> or 4X<sub>h</sub> depending on the DEV ID switch see section 3.4.1 (where X is the address value, 0 through F).

### 6.1. Software Message (ES-Key designation 0x1X to 0x1E or 0x4X to 0x1E)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	30
Source addr:	16-31 or 64-79	Message frequency:	10 per second

Byte 0 – Inputs 0 through 7 state (input 0 is in the LSb position)  
 Byte 1 – Output Active State (Output 0 is in the LSb position)  
 Byte 2 – Output No Load State (Output 0 starts in high nibble)  
 Byte 3 – Output No Load State (Output 4 is in the LSb position)

### 6.2. Software Message (ES-Key designation 0x1X to 0xFF or 0x4X to 0xFF)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	255
Source addr:	16-31 or 64-79	Message frequency:	10 per second

Byte 4 – Device ID (high nibble = Device Type, low nibble = Address)  
 Byte 5 – Software version (high nibble = major rev, low nibble = minor rev)  
 Byte 7 – Error Code (Only reporting output errors)

Error Number	Description
6 – 17	Outputs Errors ( 0 – 11 )

### 6.3. Software Message (ES-Key designation 0x1X to 0xAA or 0x4X to 0xAA)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	170
Source addr:	16-31 or 64-79	Message frequency:	10 per second

Byte 0 – Dipswitch bank 0 (OUT 8, OUT 9, OUT 10, OUT 11)  
 Byte 1 – Dipswitch bank 1 (POL 0, POL 1, POL 2, POL 3, POL 4, POL 5, POL 6, POL 7)  
 Byte 2 – Dipswitch bank 2 (DEV ID, AUX 1, AUX 2, AUX 3, AUX 4, AUX 5, AUX 6, AUX 7)  
 Byte 3 – N/A  
 Byte 4 – MFI Input 4 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 5 – MFI Input 5 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 6 – MFI Input 6 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 7 – MFI Input 7 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)



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**6.4. Software Message (ES-Key designation 0x1X to 0xAB or 0x4X to 0xAB)**

Priority: 6 Datapage: 0  
 PDU Format: 239 PDU Specific: 171  
 Source addr: 16-31 or 64-79 Message frequency: 10 per second

Byte 0 – MFI Input 4 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 1 – MFI Input 5 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 2 – MFI Input 6 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 3 – MFI Input 7 Mode (DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)  
 Byte 4 – System Voltage High Byte  
 Byte 5 – System Voltage Low Byte

**6.5. Software Message (ES-Key designation 0x1X to 0xAC or 0x4X to 0xAC)**

Priority: 6 Datapage: 0  
 PDU Format: 239 PDU Specific: 171  
 Source addr: 16-31 or 64-79 Message frequency: 10 per second

Byte 0 – MFI Input 4 Data Low Byte  
 Byte 1 – MFI Input 4 Data High Byte  
 Byte 2 – MFI Input 5 Data Low Byte  
 Byte 3 – MFI Input 5 Data High Byte  
 Byte 4 – MFI Input 6 Data Low Byte  
 Byte 5 – MFI Input 6 Data High Byte  
 Byte 6 – MFI Input 7 Data Low Byte  
 Byte 7 – MFI Input 7 Data High Byte



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## 7. Device Network RX CAN messages

The ES-Key device ID for the module is 1X<sub>h</sub> or 4X<sub>h</sub> depending on the DEV ID switch see section 3.4.1 (*where X is the address value, 0 through F*).

### 7.1. Software Message (ES-Key designation 0x1E to 0x1X or 0x1E to 0x4X)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	16-31 or 64-79
Source addr:	30	Message frequency:	as received

Byte 0 – Outputs 0 through 7 state (Output 0 is in the LSb position)

Byte 1 – Flash 0 through 3 state (Flash 0 starts in high nibble)

Byte 2 – Flash 4 through 7 state (Flash 4 is in the LSb position)

Byte 3 – PWM States 0 through 7 state (PWM 0 is in the LSb position)

Byte 2 bit 4 selects the flash rate 75 or 100 hz

### 7.2. Software Message (ES-Key designation 0x1E to 0xFF)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	255
Source addr:	30	Message frequency:	as received

Byte 0 – Outputs 0 through 7 default state (Output 0 is in the LSb position)

### 7.3. Software Message (ES-Key designation 0x1X to 0xFF or 0x4X to 0xFF)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	255
Source addr:	16-31 or 64-79	Message frequency:	as received

This message is simply used to check for a conflicting module having been set for the same address to determine the proper handling of the communication diagnostic LED (see section 8).



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## 7.4. Software Message (ES-Key designation 0xAB to 0x1X or 0xAB to 0x4X)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	16-31or 64-79
Source addr:	171	Message frequency:	as received

Byte 0 – Function

Byte 1 – Security Byte (Always 0x23)

Byte 2 – Channel (0-7)

Byte 3 – Data Low Byte

Byte 4 – Data High Byte

### Byte 0 Function Table

Test Message Enable	0x35	( 0x01 enables 0x00 disables)
Calibrate Output no-load state	0x40	
Calibrate Output Min Load	0x41	
Calibrate Output Max Load	0x42	
Set Defaults	0x43	
Set Output Current Trip Point	0x44	( example 7.5 amps send value of 750)
Set Output Startup Duty Cycle	0x45	(used to set the startup duty cycle value is saved to EEprom)
Enable Channel PWM Soft Start	0x46	(0x01 enables 0x00 disables)
Enable Channel PWM Soft Stop	0x47	(0x01 enables 0x00 disables)
Set PWM Soft Start time	0x48	(Value x .01 determines duty cycle increase rate)
Set PWM Soft Stop time	0x49	(Value x .01 determines duty cycle decrease rate)
Set Output Active PWM Duty Cycle	0x50	(Allows output to change active duty cycle 0 - 100%)

## 7.5. Software Message (ES-Key designation 0xAA to 0x1X or 0xAA to 0x4X)

Priority:	6	Datapage:	0
PDU Format:	239	PDU Specific:	16-31or 64-79
Source addr:	170	Message frequency:	as received

Byte 0 – Function

Byte 1 – Security Byte (Always 0x45)


Byte 2 – Channel (4-7)

Byte 3 – Data Low Byte

Byte 4 – Data High Byte

### Byte 0 Function Table

Set MFI Mode	0x70	
Set MFI Mode Data		(DIG = 0, 4-20 = 1, 0-5 = 2, 0-30 = 3, THERM = 4, FREQ = 5)

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## 8. Diagnostics

The Power Distribution module has 3 diagnostic LEDs which are viewable through the top of its amber enclosure.

**PWR** - +5VDC logic power  
**BUS** - +9...+32VDC Module power  
**COM** - Module status indicator

The COM LED indicates the module's CAN communication status.

### **On Solid**

Module on-line

### **Flashing slow (2Hz)**

CAN bus okay, but the module is not receiving messages from the Universal System Manager (USM).

### **Flashing fast (8Hz)**

CAN bus error, no communications or not connected.

### **Flashing fast (20Hz)**

Output Over Current Indication

### **Double flash**

CAN bus has an *ACTIVE* error, no communications.





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## 9. Glossary

<b>PDM</b>	<u>P</u> ower <u>D</u> istribution <u>M</u> odule
<b>LED</b>	<u>L</u> ight <u>E</u> mitting <u>D</u> iode. The lights on the display used to show tank level and information.
<b>CAN</b>	<u>C</u> ontroller <u>A</u> rea <u>N</u> etwork. SAE J1939 communication method.
<b>EEPROM</b>	<u>E</u> lectrically <u>E</u> rasable <u>P</u> rogrammable <u>R</u> ead- <u>O</u> nly <u>M</u> emory. The memory of the tank level display, used to store the display information (tank level points, display type, dim value, etc).
<b>OEM</b>	<u>O</u> riginal <u>E</u> quipment <u>M</u> anufacturer.
<b>SAE</b>	<u>S</u> ociety of <u>A</u> utomotive <u>E</u> ngineers.
<b>ESD</b>	<u>E</u> lectro <u>S</u> tatic <u>D</u> ischarge.
<b>IP</b>	<u>I</u> ngress <u>P</u> rotection (IP 67, etc).
<b>p/n</b>	part number
<b>MFI</b>	<u>M</u> ulti <u>F</u> unction <u>I</u> nput

## 10. Technical details

Product category	ES-KEY
Voltage range	+9VDC...+32VDC
Power consumption	Supply+ input (stud)
@13.8VDC	65mA <sup>(1)</sup>
@27.6VDC	85mA <sup>(1)</sup>
Output current capability	7.5A per output positive .25A per output negative
Input current draw	2mA per input (positive or ground polarity)
Operational temperature range	-40°C...+85°C
Environmental range	IP 67
CAN specification	SAE J1939 proprietary, 250 Kbits/second
Protection	Reverse voltage protection (stud and pin 12)
	CAN buses protected to 24V
	ESD voltage protected to SAE J1113 specification for heavy duty trucks (24V)
	Transient voltage protected to SAE J1113 specification for heavy duty trucks (24V)
	Load dump voltage protected to SAE J1113 specification for heavy duty trucks (24V)
Dimensions (W x L x H) in inches [mm]	4.680 [118.80] x 5.240 [133.10] x 1.420 [36.07]

<sup>(1)</sup> Does not include current draw due to outputs connected to external loads.

### 10.1. WEEE (Waste of Electrical and Electronic Equipment) directive



This symbol [crossed-out wheeled bin WEEE Annex IV] indicates separate collection of waste electrical and electronic equipment in the European Union countries.

Please do not throw the equipment into the domestic refuse.

Each individual European Union member state has implemented the WEEE regulations into national law in slightly different ways. Please follow your national law when you want to dispose of any electrical or electronic products.

**More details can be obtained from your national WEEE recycling agency.**